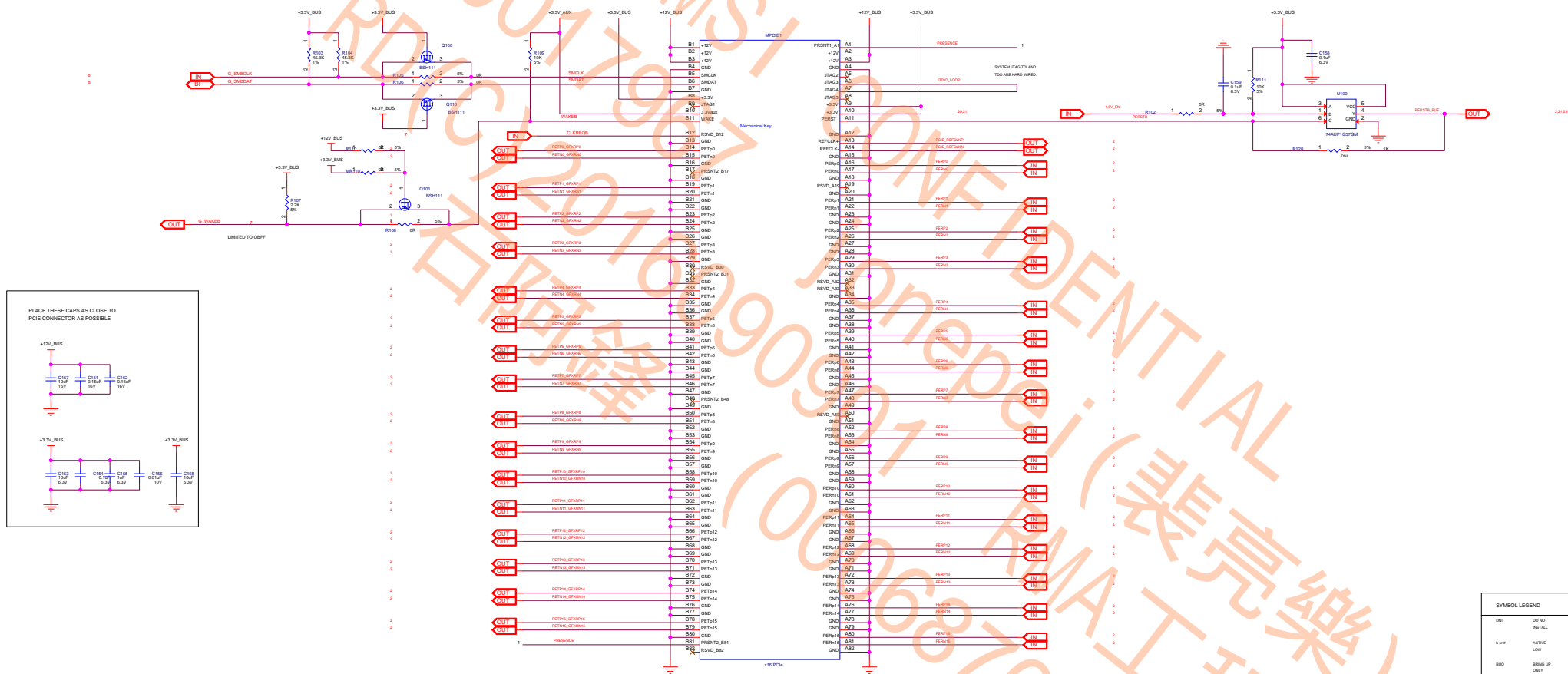

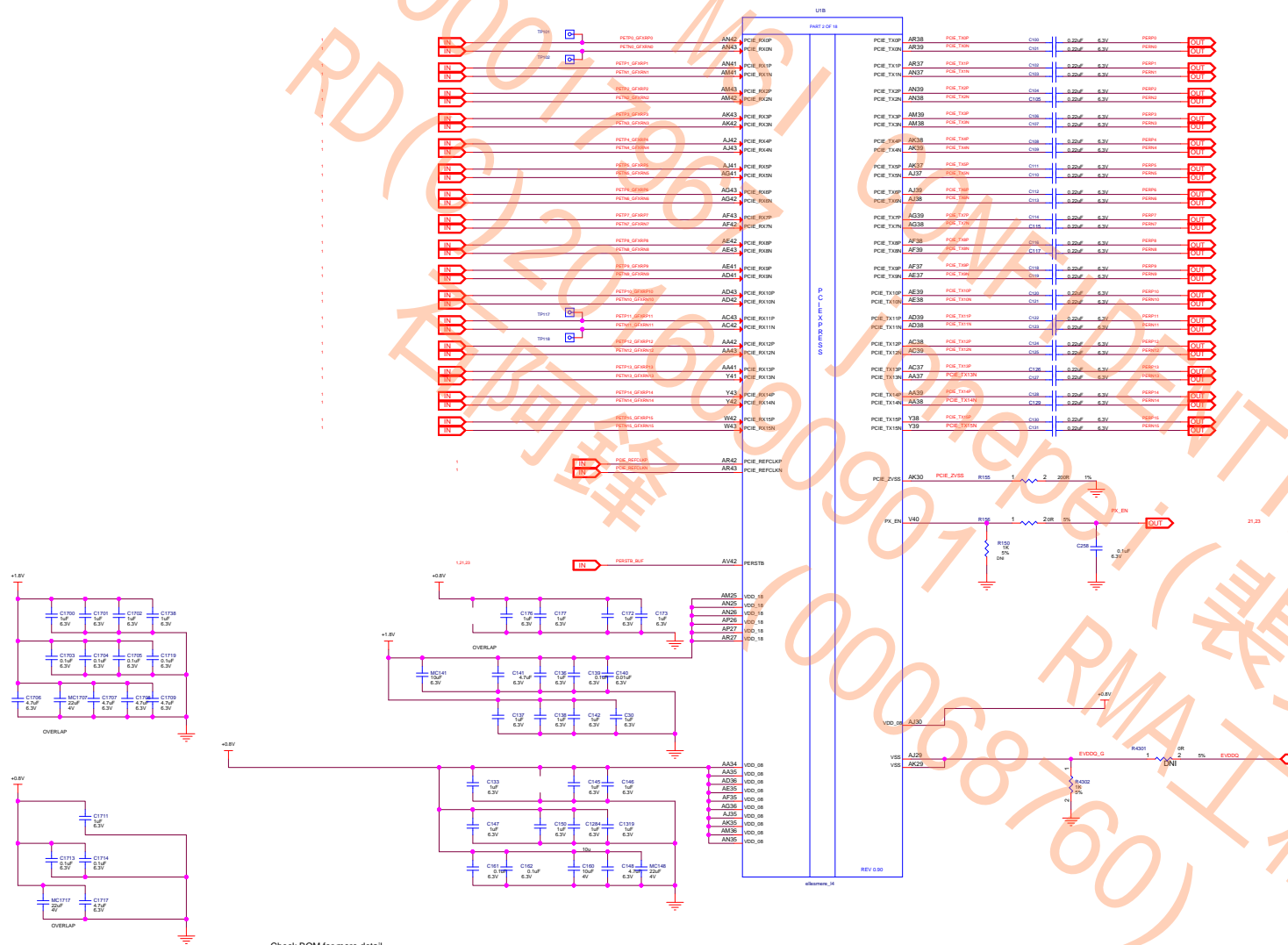


### (1) PCI-EXPRESS EDGE CONNECTOR

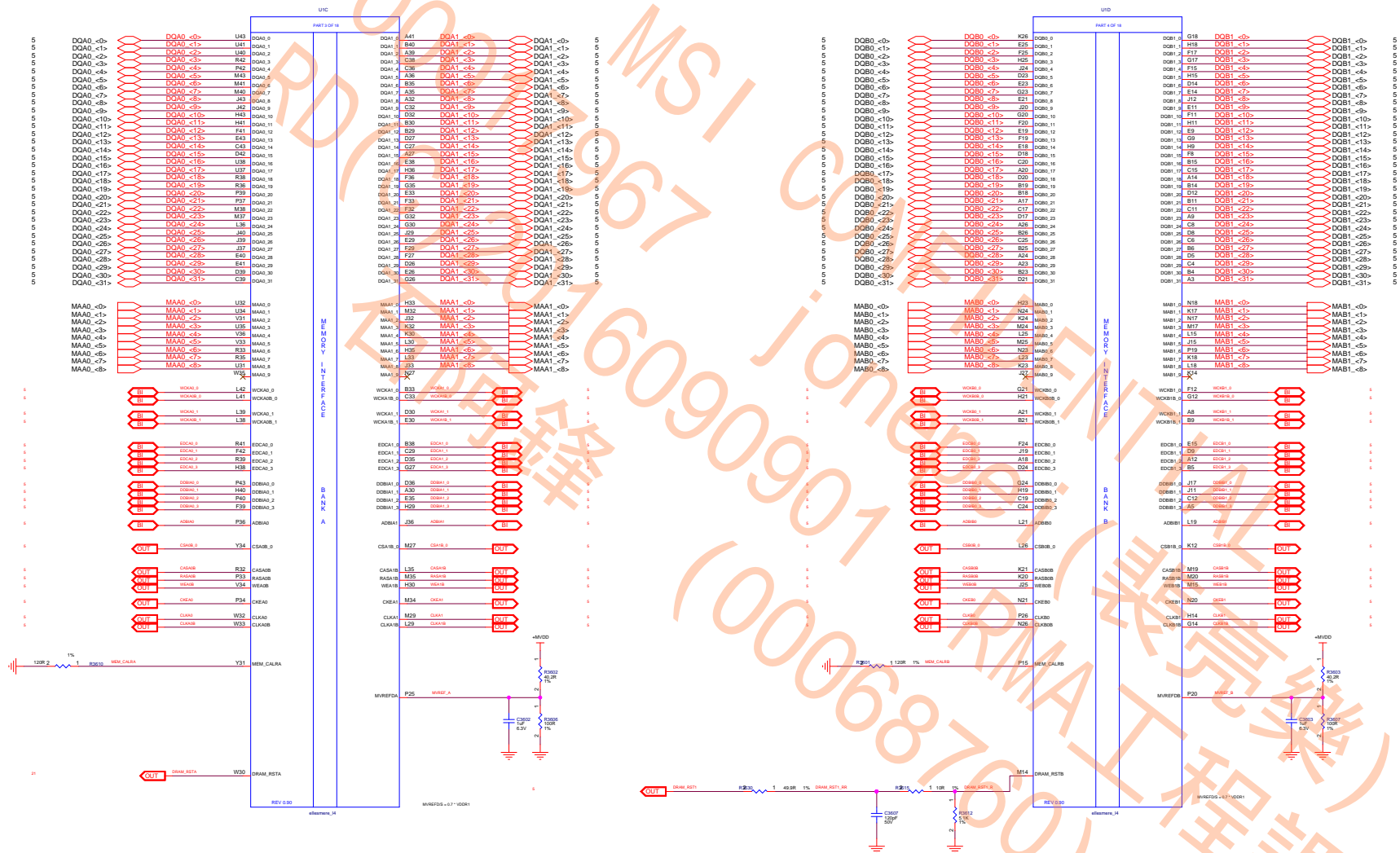


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## (2) ELLESMERE PCIE INTERFACE



## (3) ELLESMERE MEM INTERFACE CH A/B

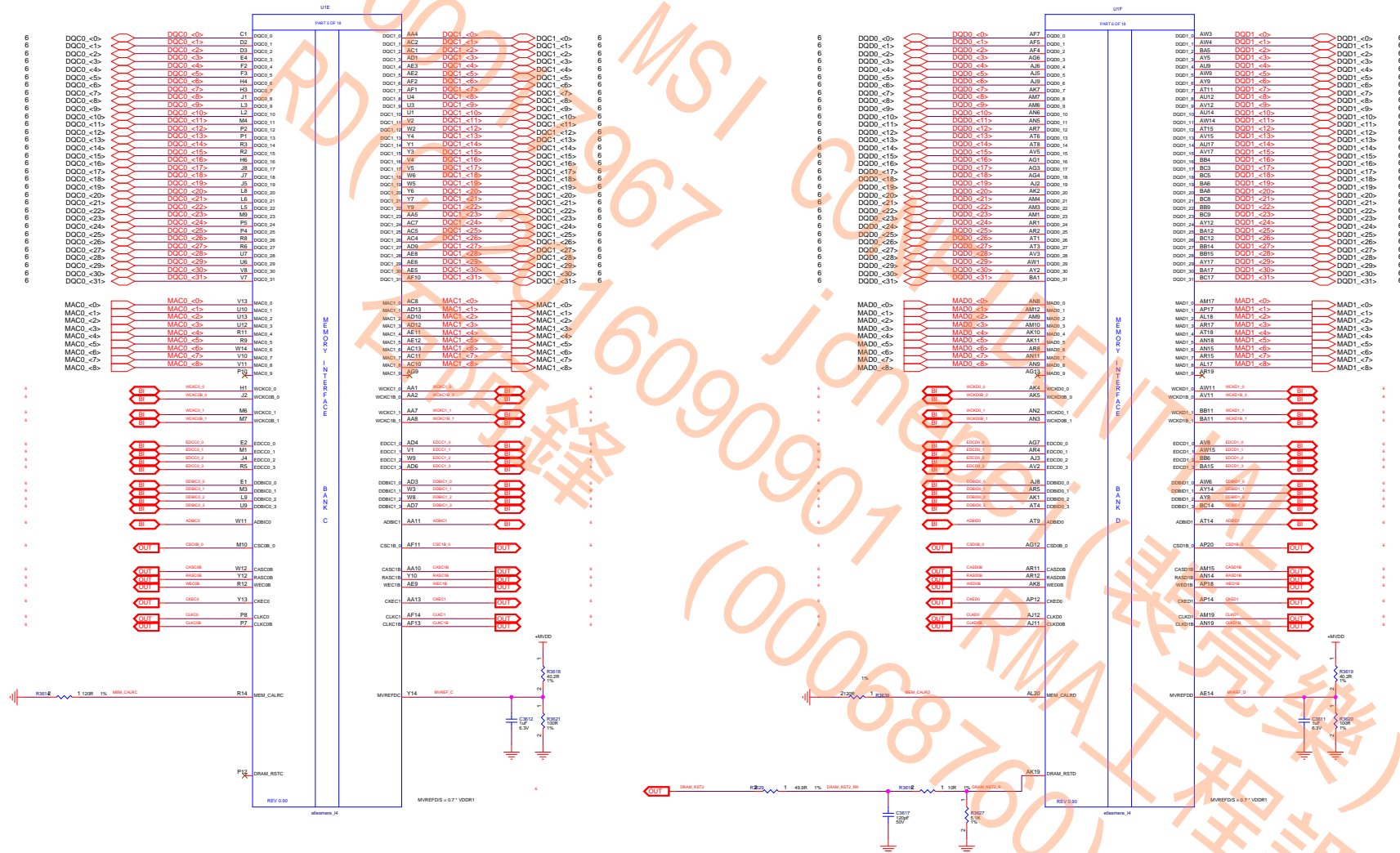


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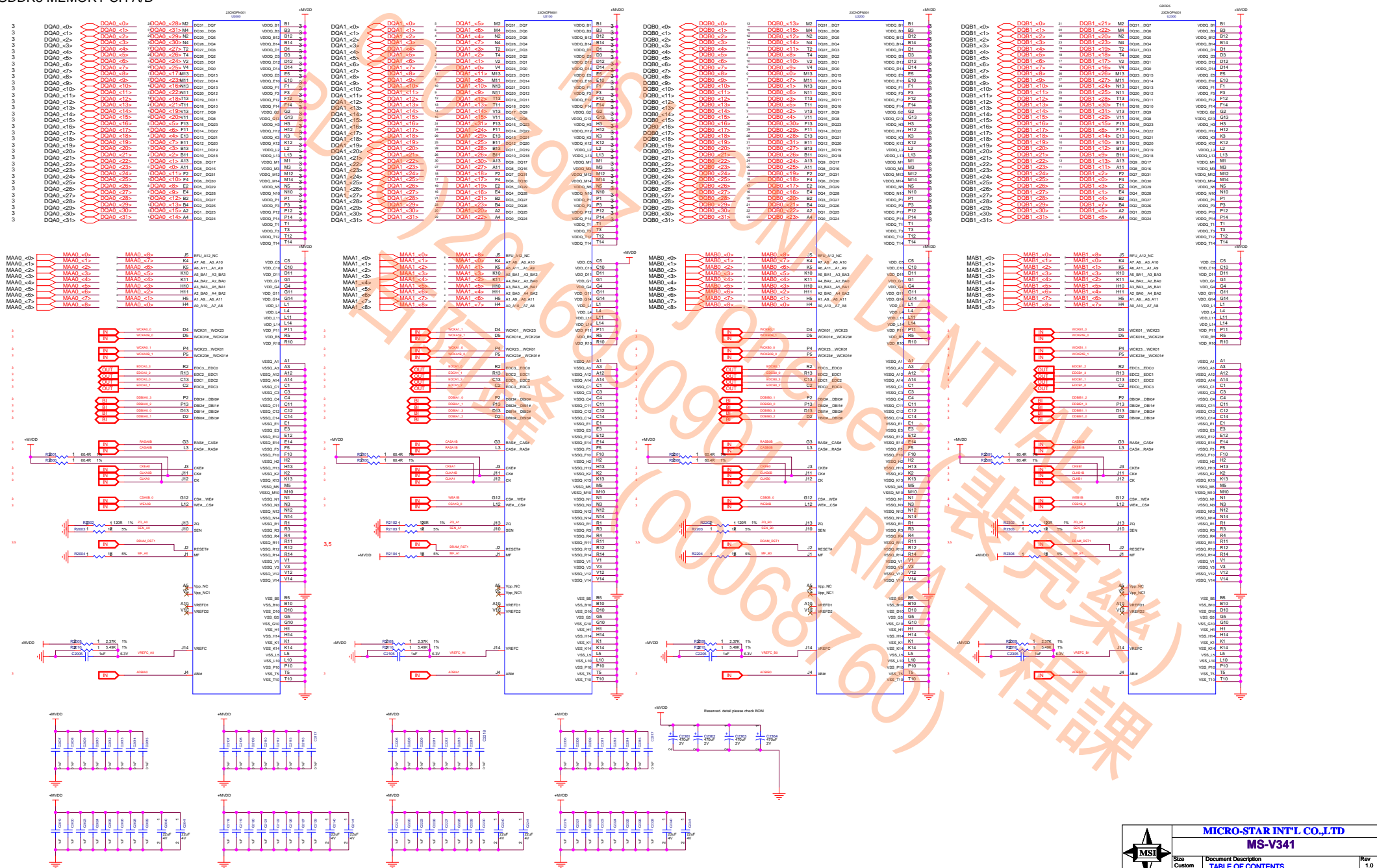
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#### (4) ELLESMERE MEM INTERFACE CH C/D

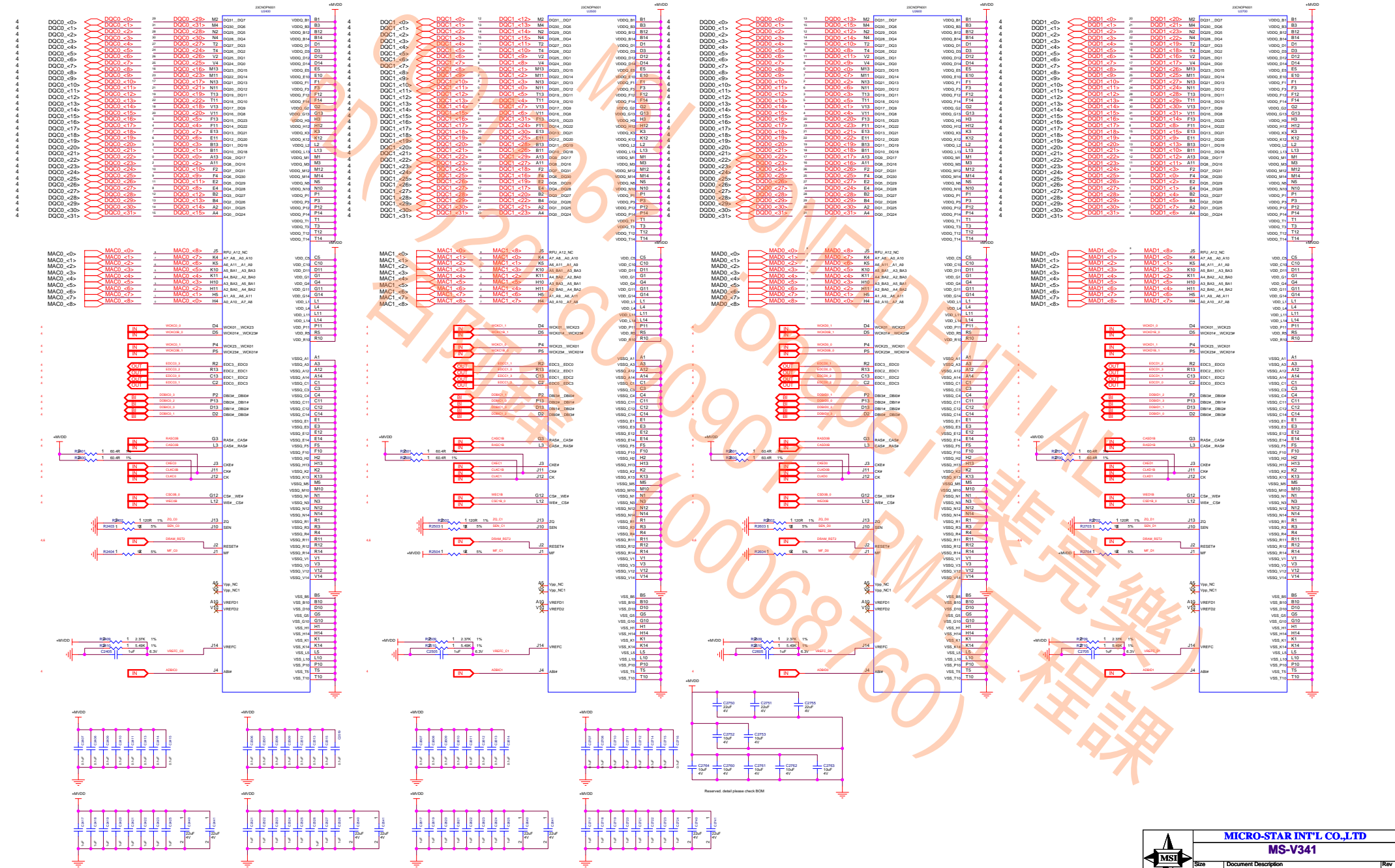


(5) GDDR5 MEMORY CH A/B





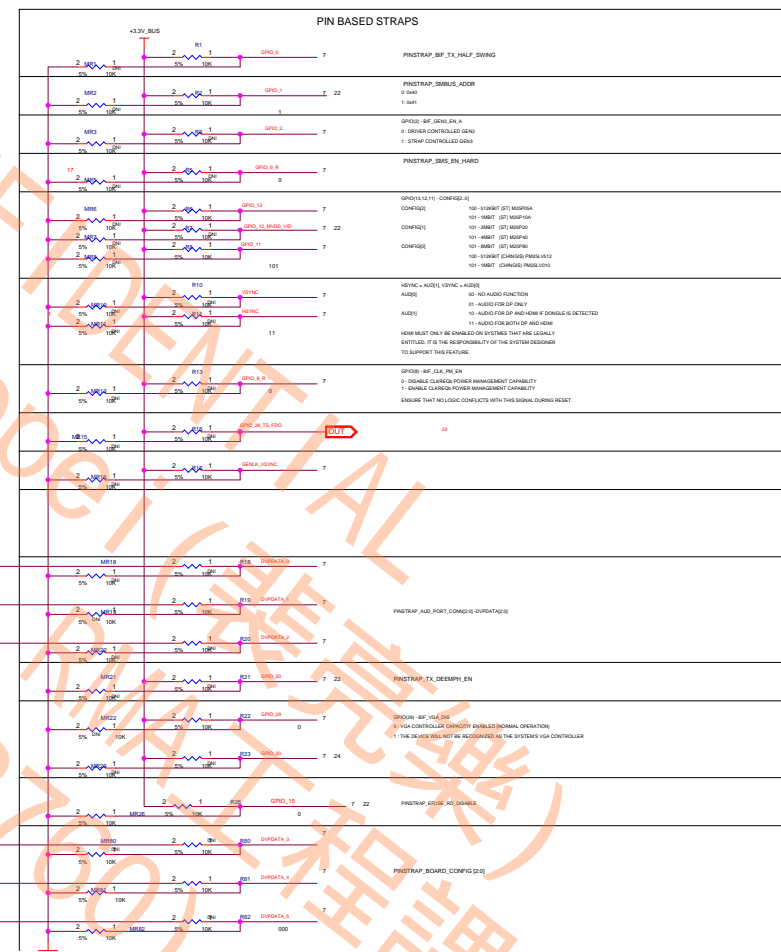
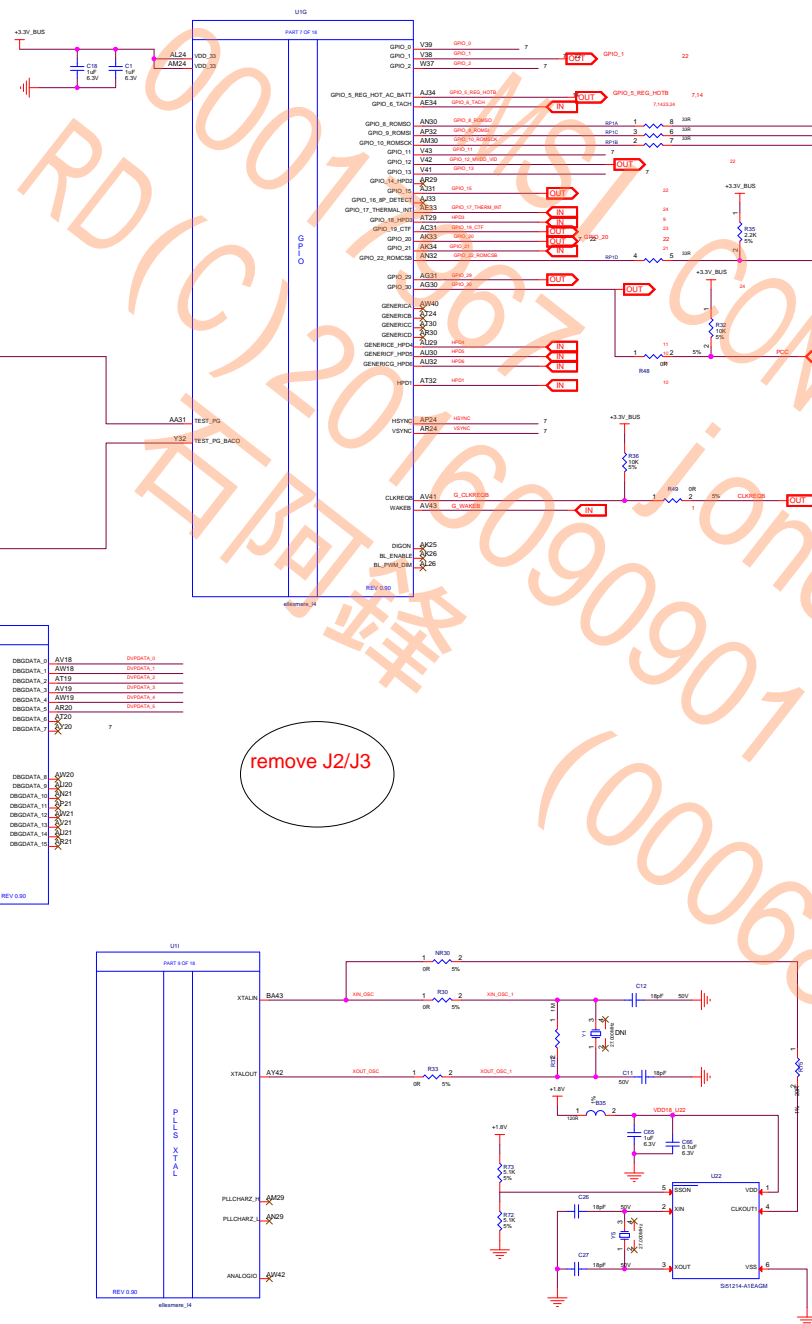
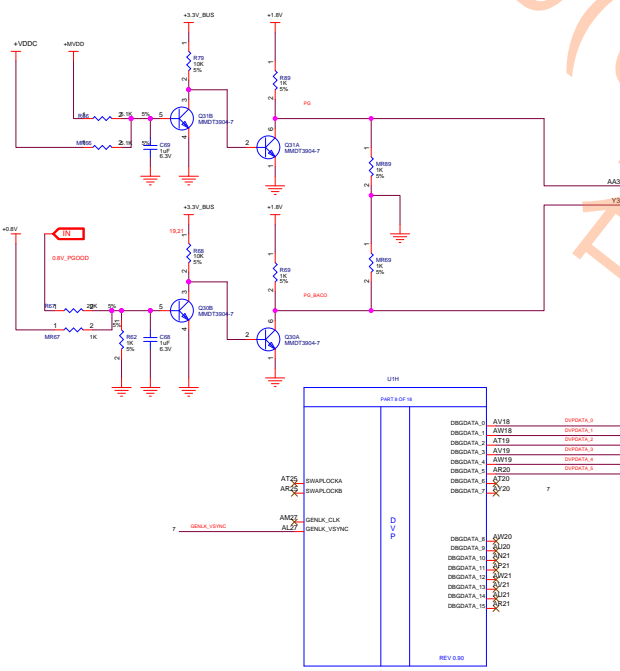
(6) GDDR5 MEMORY CH C/D



(7) ELLESMERE GPIO STRAP CF XTAL

DC ADDRESS	FUNCTION	DEVICE

DC ADDRESS	FUNCTION	DEVICE
0x98	EXT TEMP SENSOR	LM56063



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The diagram illustrates the PCB layout for a USB connector (J1) and its connection to a microcontroller (AM23). The connector pins are labeled as follows:

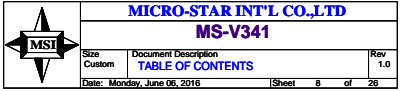
- Pin 1:** GND
- Pin 2:** D-
- Pin 3:** D+
- Pin 4:** GND
- Pin 5:** VCC
- Pin 6:** NC
- Pin 7:** NC
- Pin 8:** NC
- Pin 9:** NC
- Pin 10:** NC
- Pin 11:** NC
- Pin 12:** NC
- Pin 13:** NC
- Pin 14:** NC
- Pin 15:** NC
- Pin 16:** NC
- Pin 17:** NC
- Pin 18:** NC
- Pin 19:** NC
- Pin 20:** NC
- Pin 21:** NC
- Pin 22:** NC
- Pin 23:** NC
- Pin 24:** NC
- Pin 25:** NC
- Pin 26:** NC
- Pin 27:** NC
- Pin 28:** NC
- Pin 29:** NC
- Pin 30:** NC
- Pin 31:** NC
- Pin 32:** NC
- Pin 33:** NC
- Pin 34:** NC
- Pin 35:** NC
- Pin 36:** NC
- Pin 37:** NC
- Pin 38:** NC
- Pin 39:** NC
- Pin 40:** NC
- Pin 41:** NC
- Pin 42:** NC
- Pin 43:** NC
- Pin 44:** NC
- Pin 45:** NC
- Pin 46:** NC
- Pin 47:** NC
- Pin 48:** NC
- Pin 49:** NC
- Pin 50:** NC
- Pin 51:** NC
- Pin 52:** NC
- Pin 53:** NC
- Pin 54:** NC
- Pin 55:** NC
- Pin 56:** NC
- Pin 57:** NC
- Pin 58:** NC
- Pin 59:** NC
- Pin 60:** NC
- Pin 61:** NC
- Pin 62:** NC
- Pin 63:** NC
- Pin 64:** NC
- Pin 65:** NC
- Pin 66:** NC
- Pin 67:** NC
- Pin 68:** NC
- Pin 69:** NC
- Pin 70:** NC
- Pin 71:** NC
- Pin 72:** NC
- Pin 73:** NC
- Pin 74:** NC
- Pin 75:** NC
- Pin 76:** NC
- Pin 77:** NC
- Pin 78:** NC
- Pin 79:** NC
- Pin 80:** NC
- Pin 81:** NC
- Pin 82:** NC
- Pin 83:** NC
- Pin 84:** NC
- Pin 85:** NC
- Pin 86:** NC
- Pin 87:** NC
- Pin 88:** NC
- Pin 89:** NC
- Pin 90:** NC
- Pin 91:** NC
- Pin 92:** NC
- Pin 93:** NC
- Pin 94:** NC
- Pin 95:** NC
- Pin 96:** NC
- Pin 97:** NC
- Pin 98:** NC
- Pin 99:** NC
- Pin 100:** NC

The microcontroller (AM23) is connected to the connector pins as follows:

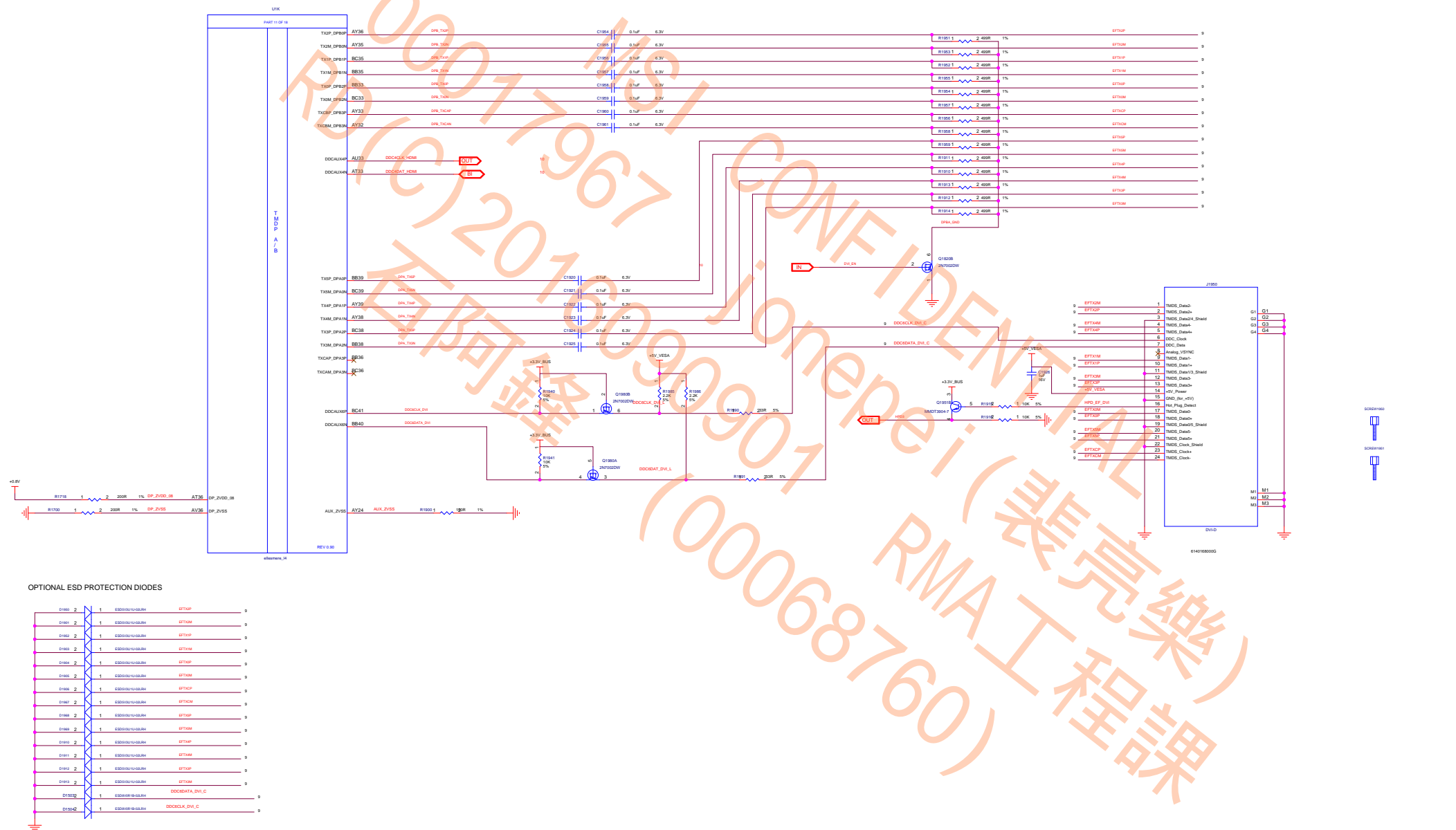
- Pin 1:** GND
- Pin 2:** D-
- Pin 3:** D+
- Pin 4:** GND
- Pin 5:** VCC
- Pin 6:** NC
- Pin 7:** NC
- Pin 8:** NC
- Pin 9:** NC
- Pin 10:** NC
- Pin 11:** NC
- Pin 12:** NC
- Pin 13:** NC
- Pin 14:** NC
- Pin 15:** NC
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- Pin 40:** NC
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- Pin 75:** NC
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- Pin 78:** NC
- Pin 79:** NC
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- Pin 90:** NC
- Pin 91:** NC
- Pin 92:** NC
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- Pin 97:** NC
- Pin 98:** NC
- Pin 99:** NC
- Pin 100:** NC

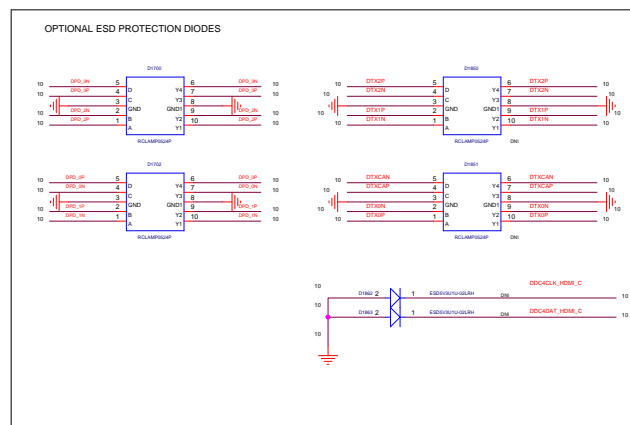
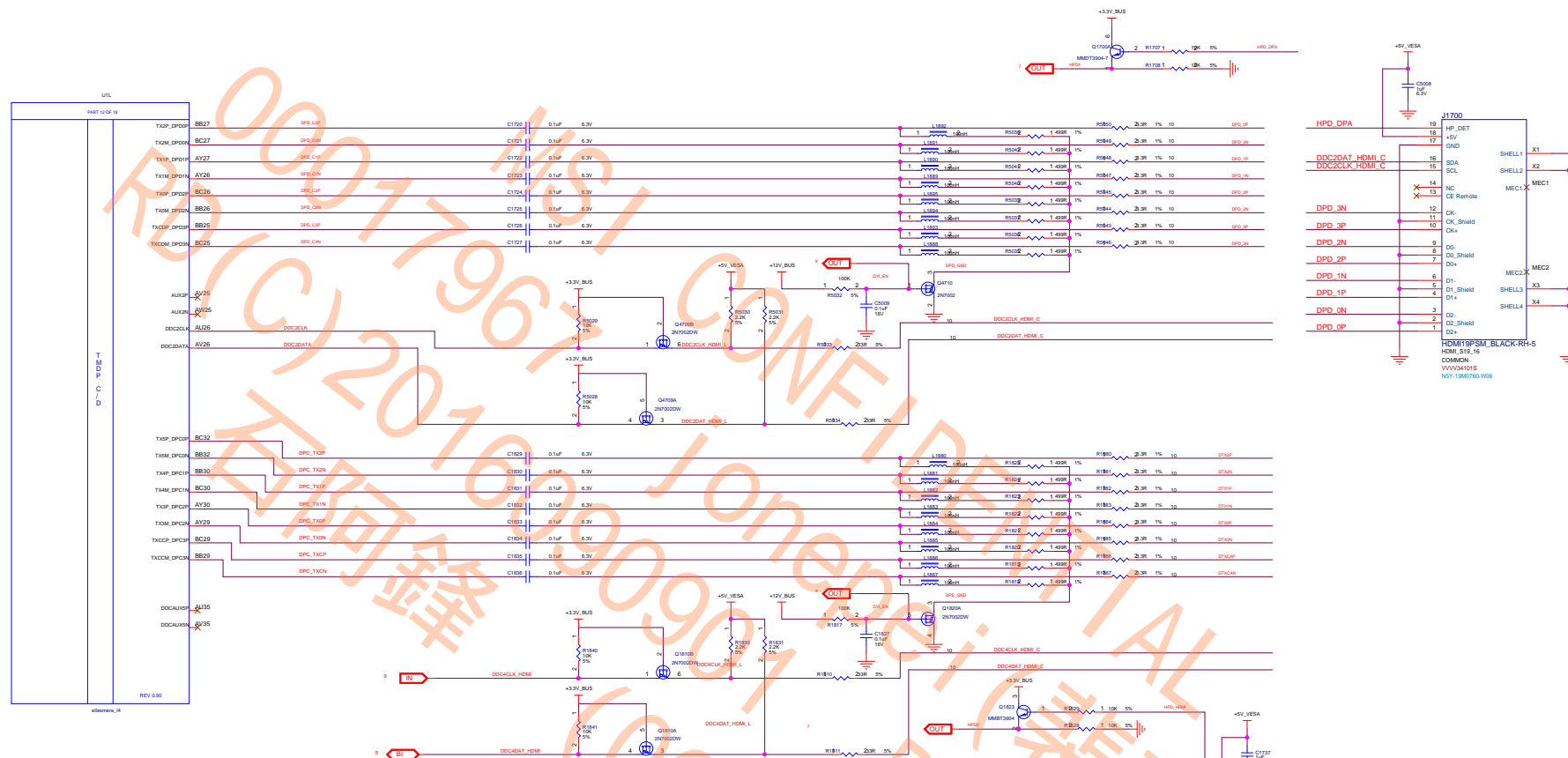
The diagram also shows the connection of the microcontroller (AM23) to the USB connector (J1) via the USB pins. The microcontroller is connected to the connector pins as follows:

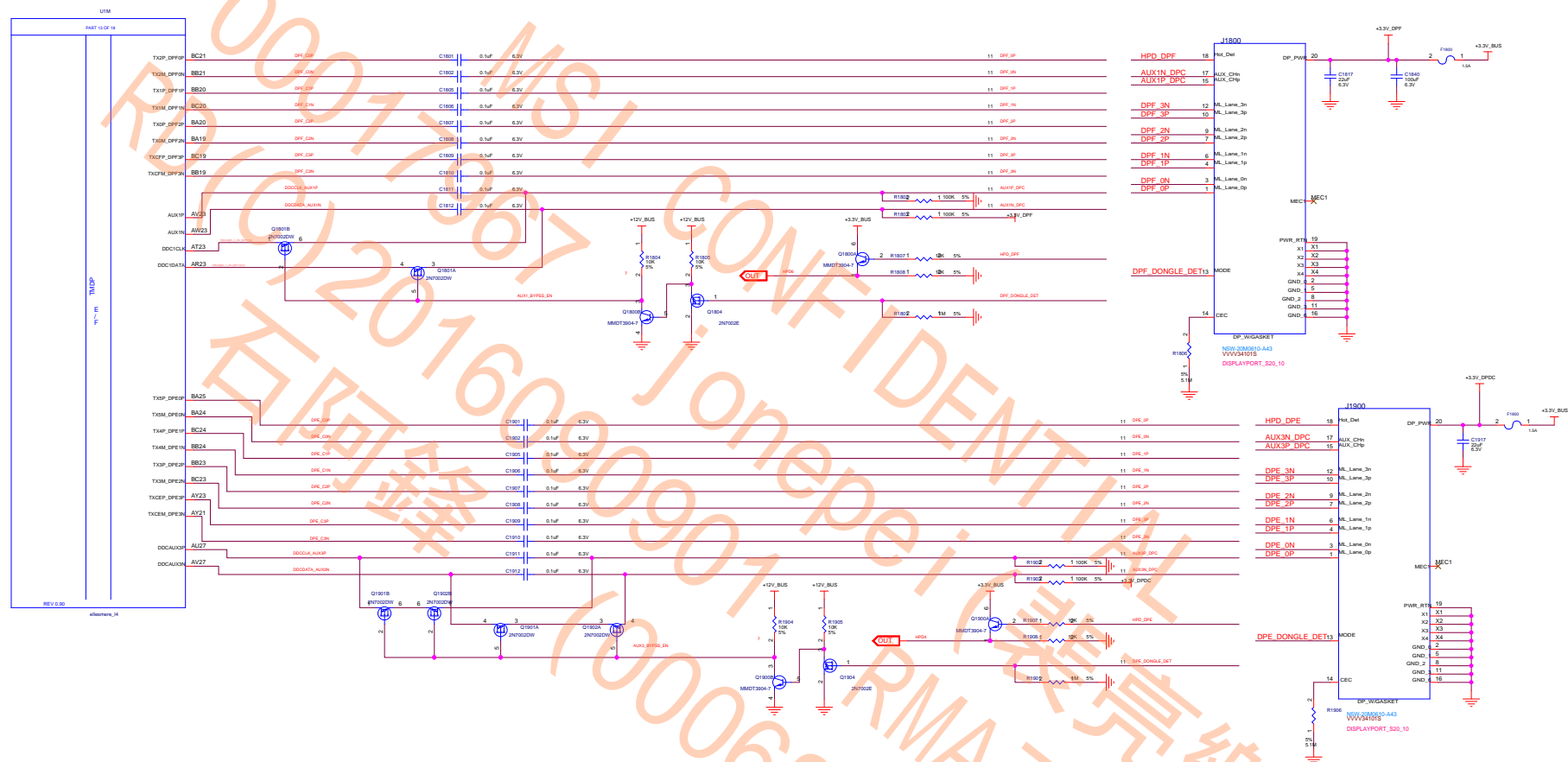
- Pin 1:** GND
- Pin 2:** D-
- Pin 3:** D+
- Pin 4:** GND
- Pin 5:** VCC
- Pin 6:** NC
- Pin 7:** NC
- Pin 8:** NC
- Pin 9:** NC
- Pin 10:** NC
- Pin 11:** NC
- Pin 12:** NC
- Pin 13:** NC
- Pin 14:** NC
- Pin 15:** NC
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- Pin 18:** NC
- Pin 19:** NC
- Pin 20:** NC
- Pin 21:** NC
- Pin 22:** NC
- Pin 23:** NC
- Pin 24:** NC
- Pin 25:** NC
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- Pin 27:** NC
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- Pin 37:** NC
- Pin 38:** NC
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- Pin 42:** NC
- Pin 43:** NC
- Pin 44:** NC
- Pin 45:** NC
- Pin 46:** NC



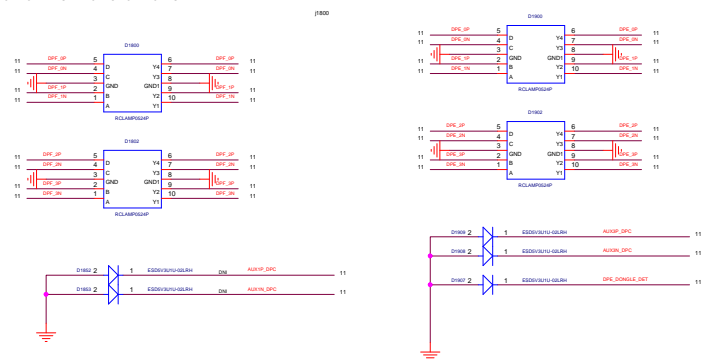




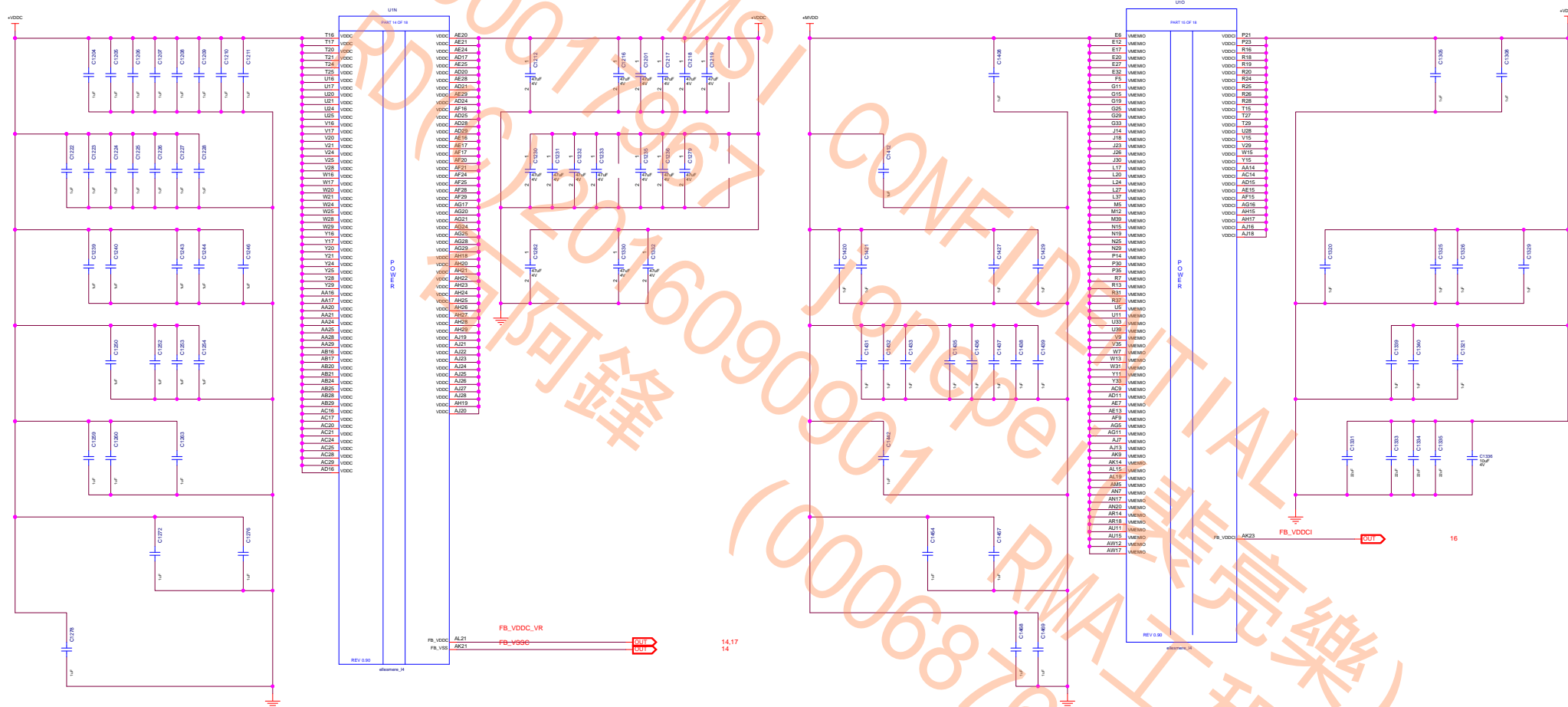


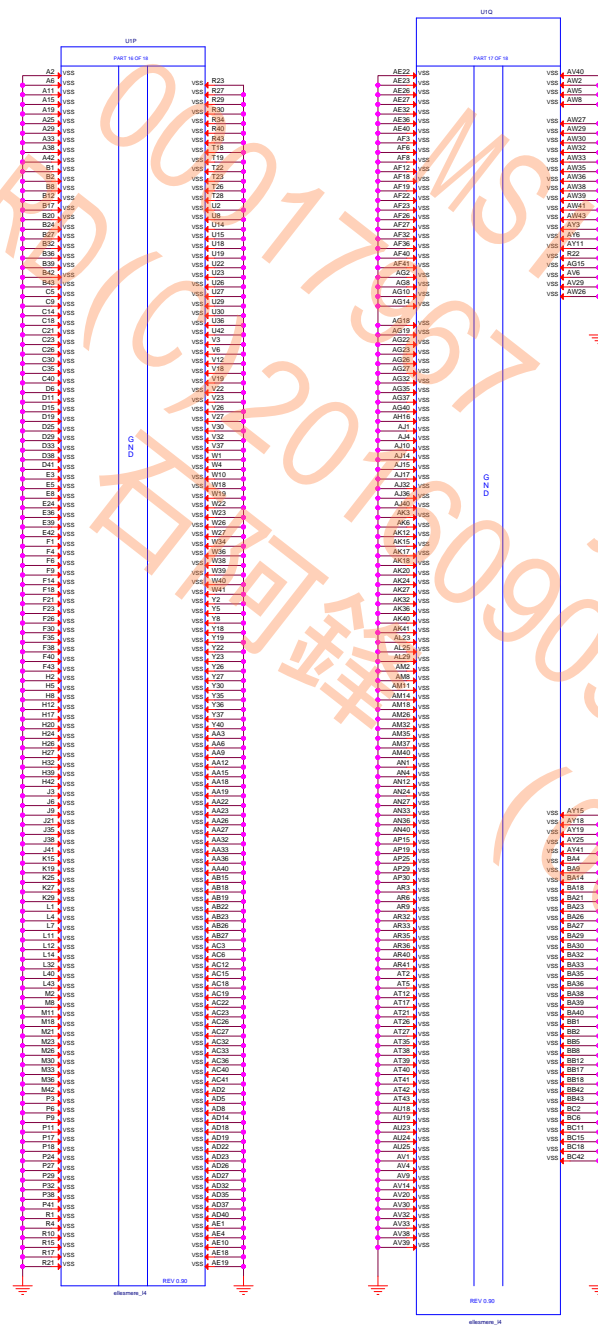


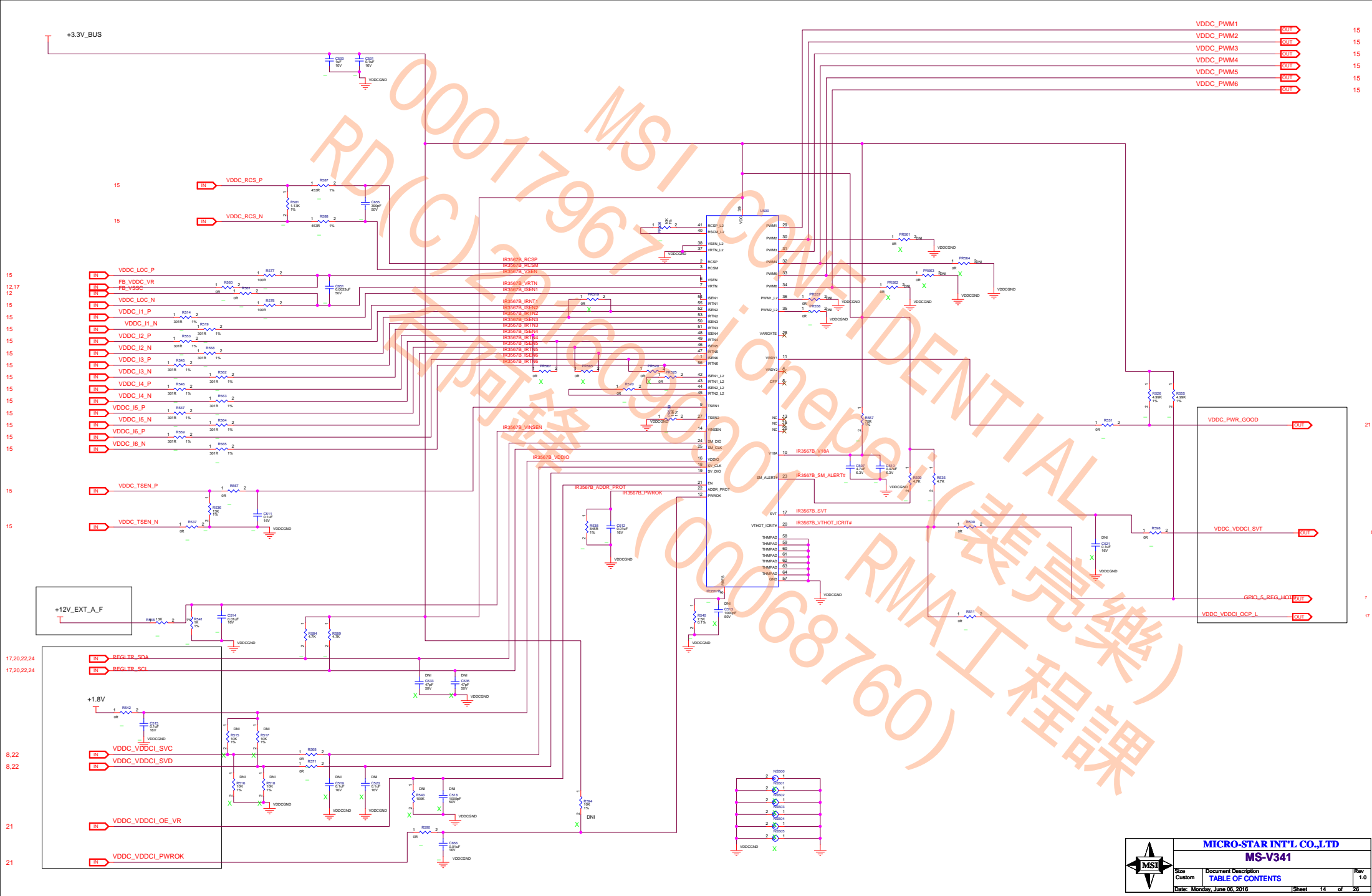
### OPTIONAL ESD PROTECTION DIODES



(12) ELLESMERE POWER

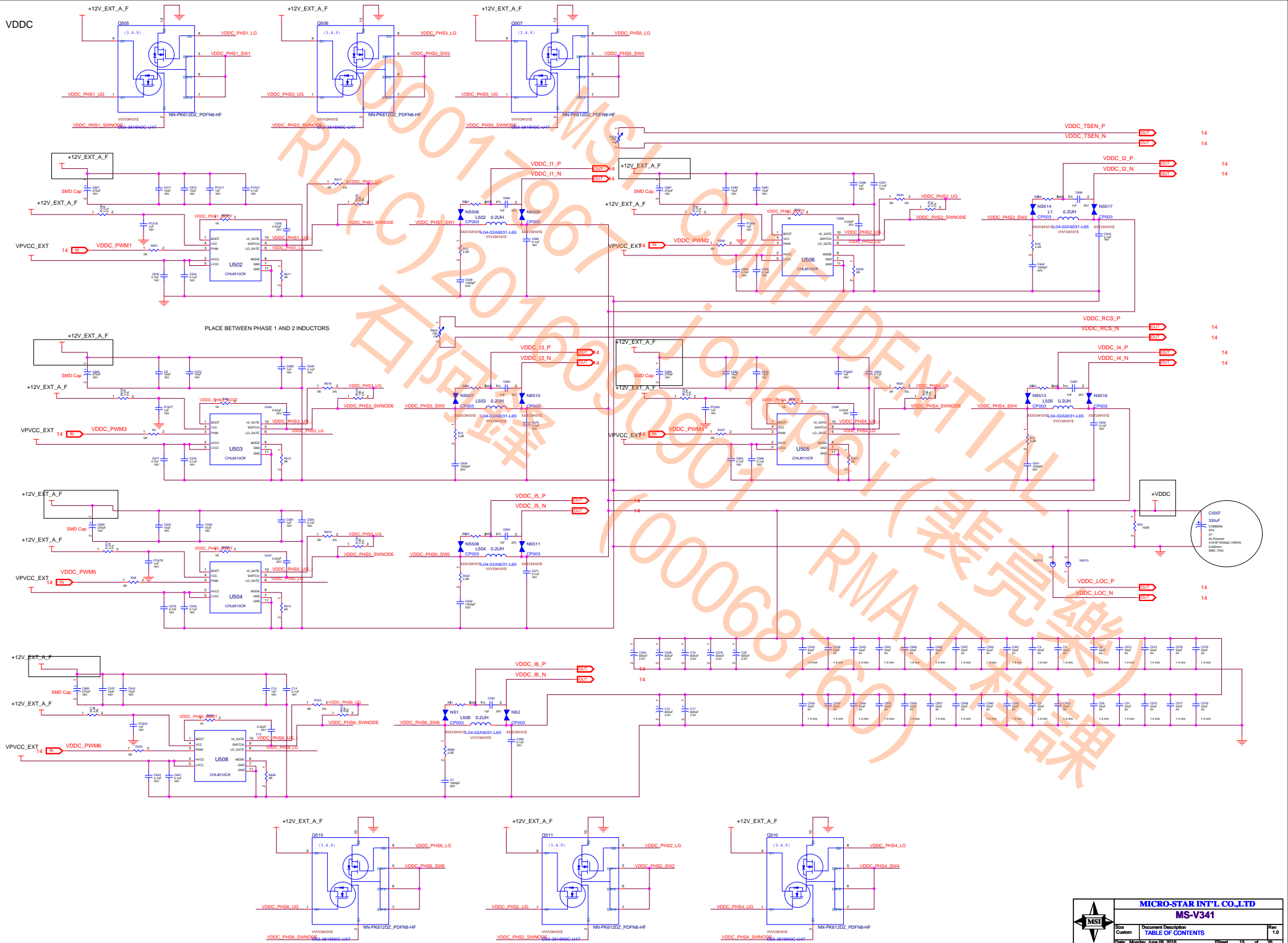






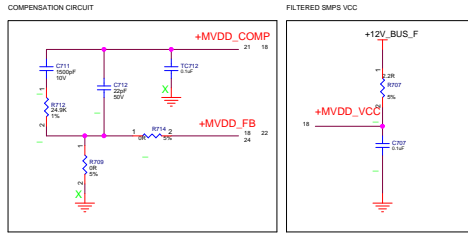
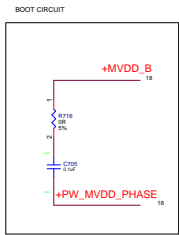


VDDC

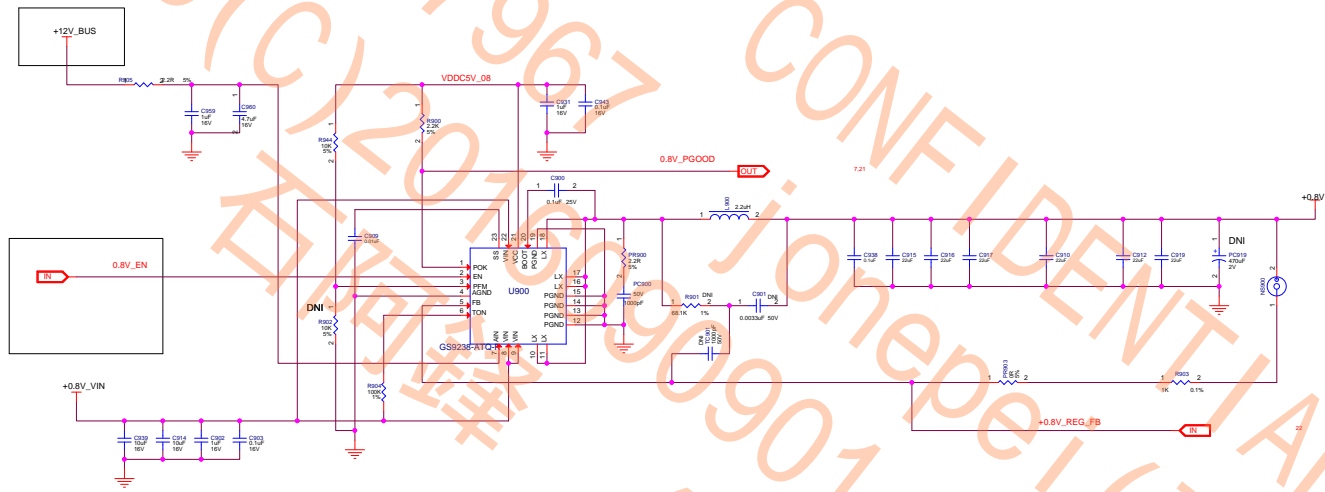






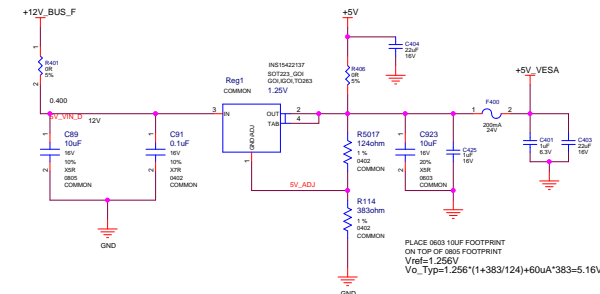


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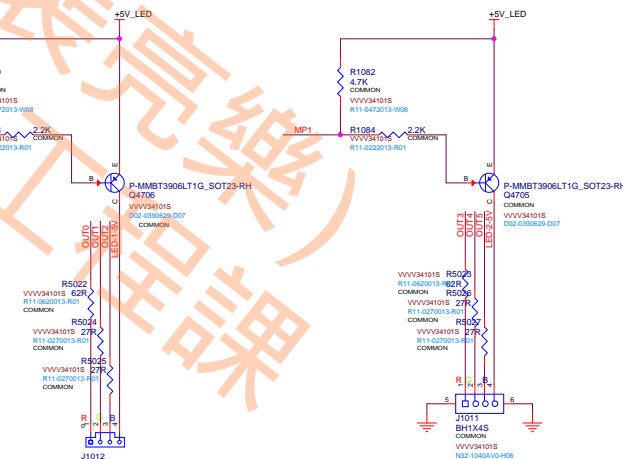
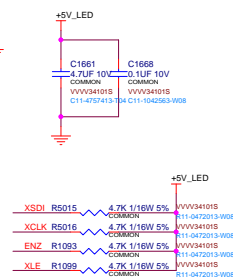
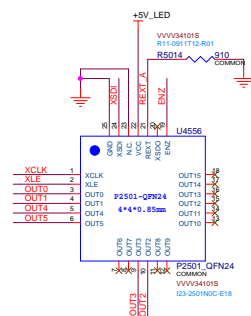
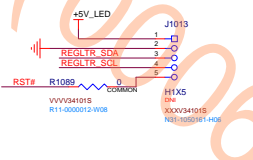


VOUT = +1.8V +/- 2%      IOUT = 1.3A RMS MAX

REGULATOR FOR +5V RAILS  
I<sub>OUT</sub> MAX = 150mA



## Firmware Programming

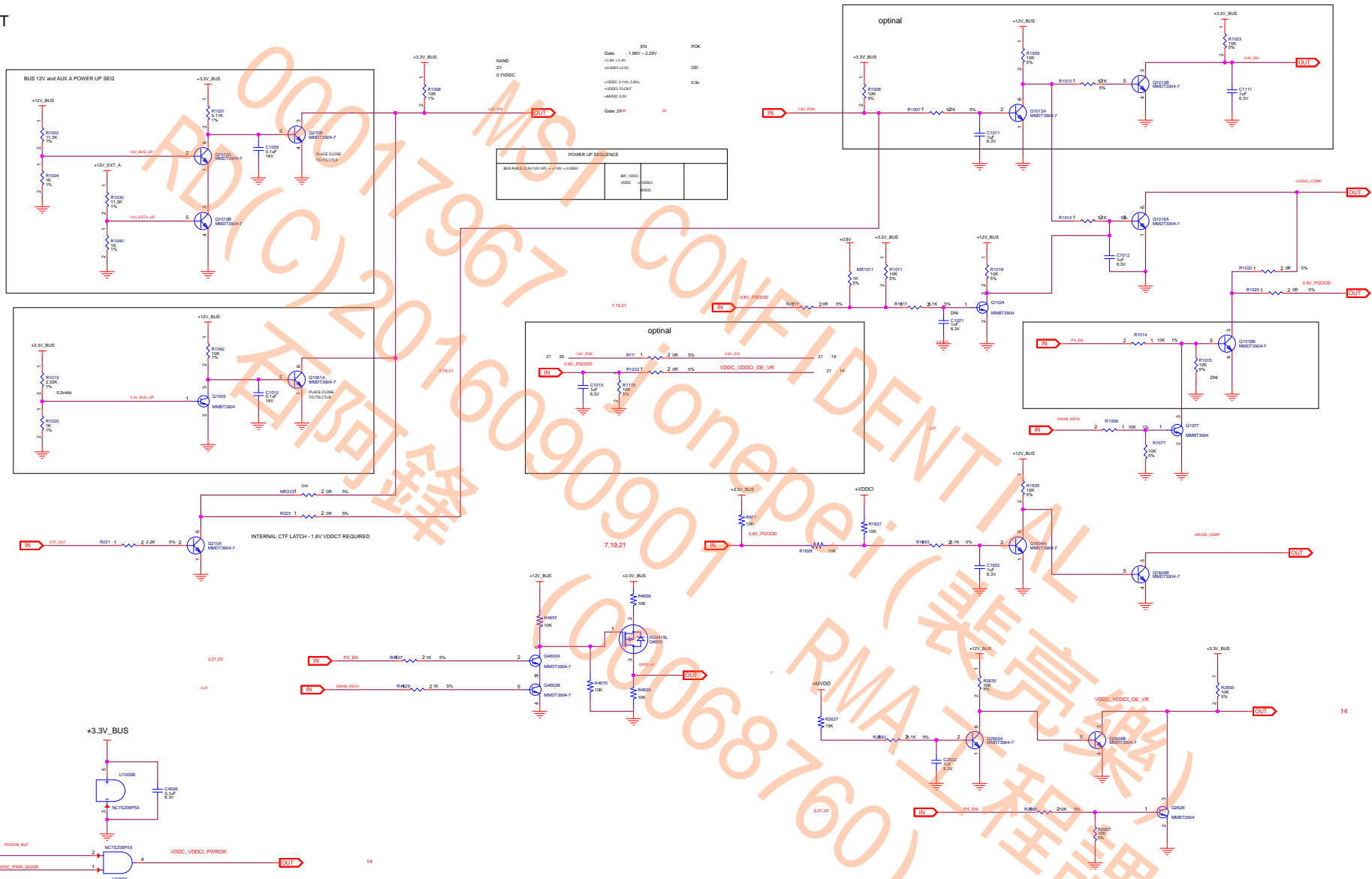
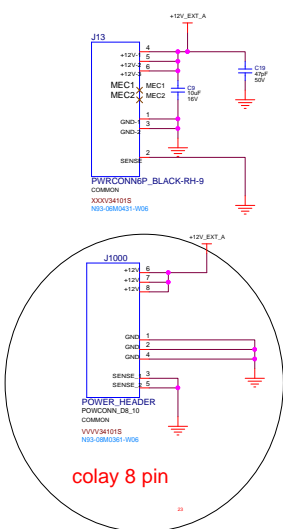


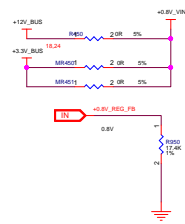
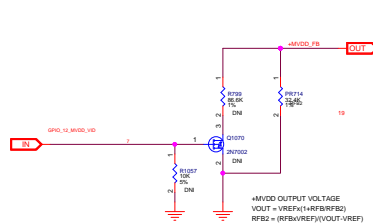
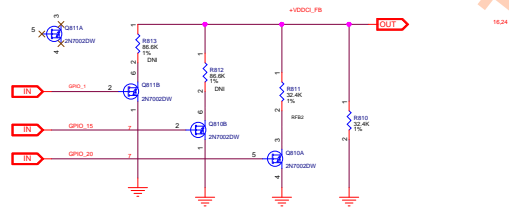
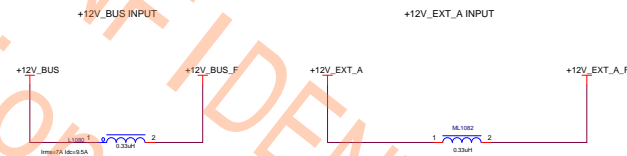
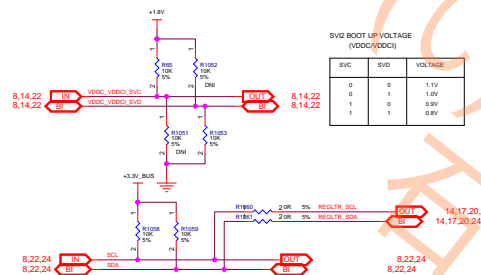
**MICRO-STAR INT'L CO.,LTD**  
**MS-V341**

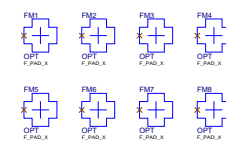
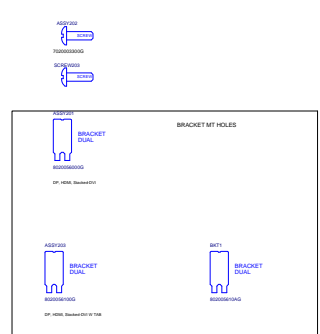
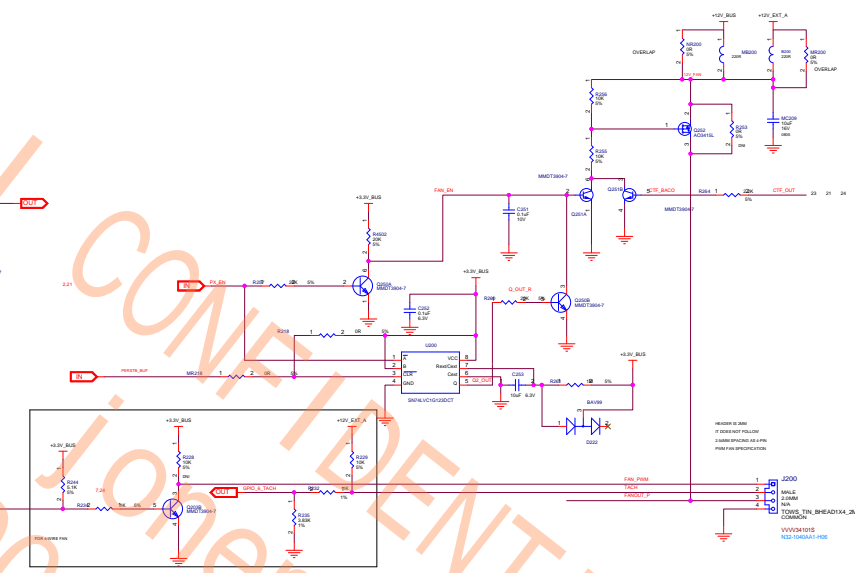
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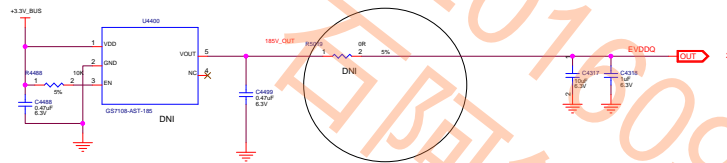
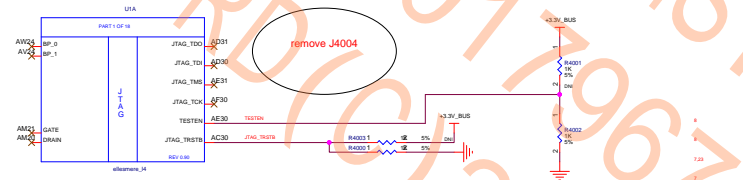
(19) POWER MANAGEMENT







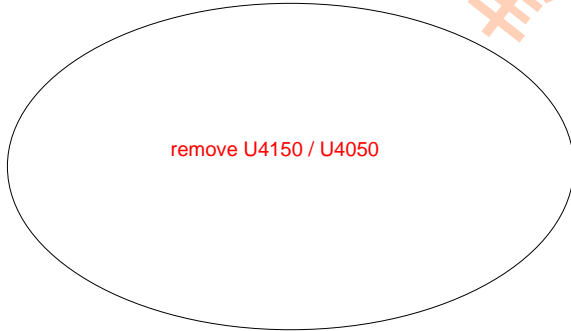
JTAG



E-FUSE CAPABILITY  
DEFAULT = GPIO-CONTROLLED  
(MANUAL OPTION AS BACK-UP)

DIGITAL POTS

DIGITAL POTS

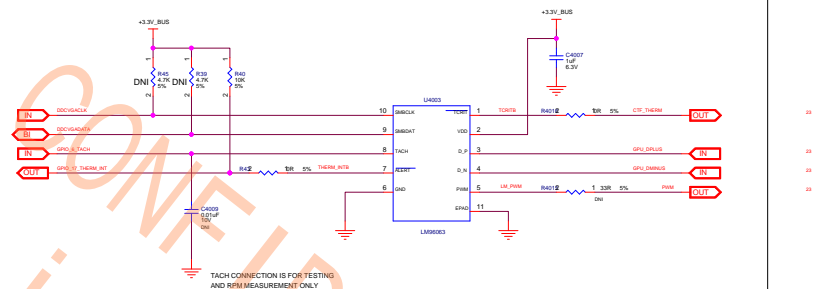


SWITCHES

remove SW

BYPASS/DISABLE CTF  
MAXIMIZE FAN

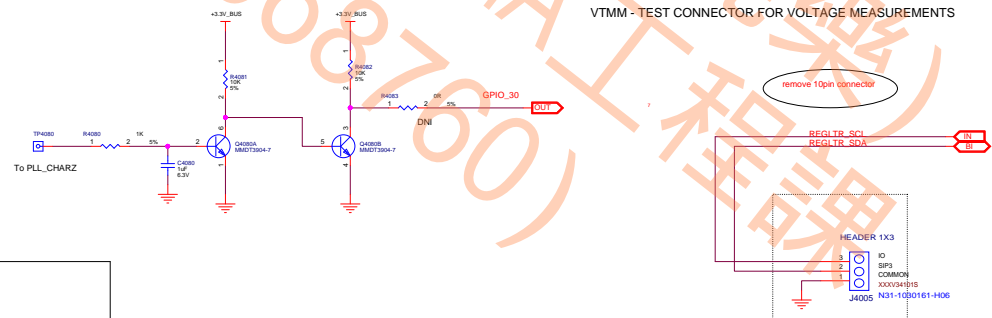
LM96163 FOR BACKUP THERMAL CONTROL



LED LIGHTS



VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS





0	00A	00000000	
1	00B	00000000	1. Add EPDVI for dynamic OSD 2. Update OSD driver signature
2	00C	00000000	HDSD: - add series resistor R11880 -R11887 - add pull-down inductor L11880 -L11887 Remove C402, C403, VR402, C412, C414, R405

- Page7: remove J2,J3
- Page9: enable DVI
- Page10: DP change to HDMI
- Page15: change dual-N MOS
- Page16: change dual-N MOS
- Page18: change dual-N MOS
- Page20: Add LED circuit
- Page21: colay 8pin power connector
- Page24: remove debug circuit